ENHANCED CIRCUIT TOPOLOGIES FOR MAXIMIZING POWER OUTPUT IN PIEZOELECTRIC ENERGY HARVESTERS

By

MOHAMMED AMANI *

AMIDU BUSARA **

*-** *Department of Electrical Engineering, Mbeya University of Science and Technology, Mbeya, Tanzania.*

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ABSTRACT

Piezoelectric energy harvesting has gained significant attention for powering small-scale electronic devices by converting mechanical vibrations into electrical energy. However, the electrical interface plays a crucial role in maximizing power transfer efficiency. This paper explores optimized circuit topologies, particularly the use of inductors to mitigate capacitive impedance effects and enhance power output. A comparative analysis of Simple Resistive Load (SRL), Inductive Load (IL), and AC-DC converter circuits is conducted, both numerically and experimentally. Results indicate that inductive circuits significantly improve power output by reducing the negative reactance of piezoelectric harvesters. The findings contribute to the development of more efficient self-powered systems for wireless sensors and low-energy electronics.

Keywords: Piezoelectric Energy, Power Optimization, Circuit, AC-DC Conversion, Passive Power, Self-Powered Electronics.

INTRODUCTION

The rapid advancement of wireless sensor networks, Internet of Things (IoT) devices, and low-power electronics has increased the demand for efficient and sustainable power sources. Traditional batteries, while widely used, pose challenges such as limited lifespan, maintenance requirements, and environmental concerns regarding disposal (Roundy et al., 2003). As a result, energy harvesting techniques have gained attention as a viable alternative for self-powered systems (Beeby et al., 2006).

Among the various energy harvesting technologies, piezoelectric energy harvesting has emerged as one of the most promising methods due to its ability to convert mechanical vibrations into electrical energy with high power density and reliability (Erturk & Inman, 2011). However, a major limitation of piezoelectric energy harvesters is their low output power due to the capacitive nature of piezoelectric materials, which introduces impedance mismatch issues and reduces power transfer efficiency (Ottman et al., 2003). Without an optimized electrical interface, much of the generated energy is dissipated rather than being effectively stored or utilized.

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The electrical interface plays a crucial role in energy harvesting efficiency. A simple resistive load (SRL) is frequently used as the most basic circuit interface, but it fails to account for the capacitive impedance of the piezoelectric transducer, resulting in significant power loss (Lefeuvre et al., 2006). To improve energy extraction, studies have explored several circuit topologies, including:

- *Inductive Load (IL) Circuits:* By introducing an inductor, the capacitive impedance of the piezoelectric harvester can be partially canceled, thereby increasing the power transfer efficiency (Lallart & Guyomar, 2008).

- *Synchronized Switch Harvesting on Inductor (SSHI):* This active switching circuit further optimizes energy transfer but requires an external power source, making it less ideal for fully self-powered systems (Richard et al., 1999).
- *AC-DC Rectification:* While rectifier circuits, such as full-wave bridge rectifiers, are commonly used to convert AC voltage into a usable DC form, they introduce voltage drops across diodes, leading to power loss (Guyomar & Lallart, 2009).

The purpose of this study is to look into passive inductance-based circuit topologies in order to get the most power out of piezoelectric energy harvesting. The key contributions include:

- *Comparative Analysis:* A detailed numerical and experimental comparison of SRL, IL, and AC-DC circuits to evaluate their power output efficiency.
- *Inductor-Based Optimization:* It is demonstrated how inductors can be leveraged to improve power transfer without requiring external energy sources.
- *Experimental Validation:* Real-world tests are conducted using both wind-driven piezoelectric flags and vibrational piezoelectric beams to validate theoretical models.

By focusing on passive circuit enhancements, this work provides a practical solution for energy harvesting in self-powered electronic systems, making it applicable to wireless sensors, biomedical implants, and other low-power IoT devices.

1. Literature Review

The development of piezoelectric energy harvesting circuits has been extensively studied, focusing on improving power extraction efficiency and impedance matching.

1.1 Energy Harvesting Technologies

Energy harvesting has emerged as a critical solution for powering low-energy electronic devices in remote or inaccessible locations, such as wireless sensor networks (WSNs), implantable biomedical devices, and Internet of Things (IoT) applications (Beeby et al., 2006; Roundy et al., 2003). Various energy sources have been explored:

- *Solar Energy Harvesting:* Photovoltaic systems provide sustainable power but are limited by weather conditions and availability of sunlight (Fröhlich et al., 2015).
- *Thermal Energy Harvesting:* Electricity is generated by utilizing temperature gradients, but large thermal gradients are required for efficient operation (Monfray et al., 2012)
- *Electromagnetic Energy Harvesting:* Magnetic field variations are converted into electrical energy, but low power density and complex circuit requirements limit its efficiency (Mitcheson et al., 2004).
- *Piezoelectric Energy Harvesting:* Mechanical strain is directly converted into electricity with high efficiency, making it ideal for vibration-based applications (Erturk & Inman, 2011; Priya & Inman, 2009).

Among these, piezoelectric energy harvesting stands out due to its compact form factor and high electromechanical coupling efficiency, making it suitable for both micro-scale and macro-scale applications (Lefeuvre et al., 2006; Ottman et al., 2003).

1.2 Circuit Interfaces for Piezoelectric Energy Harvesting

A Simple Resistive Load (SRL) is the most basic circuit interface for a piezoelectric harvester. The power output is given by:

$$P_{output} = \frac{V_o^2}{R_L}$$

Where, Vo is the output voltage and RL is the load resistance. However, SRL circuits suffer from poor impedance matching, leading to substantial energy dissipation (Lefeuvre et al., 2006; Ottman et al., 2003).

To counteract the capacitive impedance of piezoelectric harvesters, studieds have explored the use of inductors to improve power transfer efficiency. By introducing an inductor (L), the capacitive reactance of the piezoelectric harvester can be partially cancelled:

$$R_{optimal} = \frac{1}{\omega CP} - \omega L$$

Where, CP is the internal capacitance, and ω is the excitation frequency. Badel et al. (2006) and Lallart and Guyomar (2008) have demonstrated that passive inductor circuits significantly increase power extraction without requiring an external power source.

More advanced SSHI circuits use active switching mechanisms to optimize energy transfer by flipping the voltage polarity at precise moments (Guyomar & Lallart, 2009; Richard et al., 1999). While effective, these circuits require external control circuitry, increasing complexity and power consumption (Lefeuvre et al., 2006).

Most piezoelectric harvesters generate AC voltage, necessitating rectification for practical applications. A diode bridge rectifier is commonly used but suffers from voltage drops across the diodes, leading to power losses (Lefeuvre et al., 2006). To address this, Inductive AC-DC circuits integrate an inductor before rectification, reducing impedance and enhancing efficiency (Zhao & You, 2014).

1.3 Passive Inductor-Based Optimization Methods

Inductive matching techniques improve power transfer efficiency by aligning the impedance of the harvester with the load circuit (Lefeuvre et al., 2006; Mitcheson et al., 2004). The key principle is based on the maximum power transfer theorem, where maximum output occurs when:

$$Z_L = Z_S^*$$

Where, ZL is the load impedance and ZS* is the complex conjugate of the source impedance (Ottman et al., 2003).

1.3.1 Experimental Validation of Inductive Circuits

Recent experimental studies have confirmed that adding an inductor to the circuit significantly enhances the power output of piezoelectric harvesters (Lallart & Guyomar, 2008; Zhao & You, 2014). These improvements are particularly evident in low-frequency applications where capacitive effects dominate (Beeby et al., 2006).

2. Methodology

The methodology for evaluating and optimizing piezoelectric energy harvesting circuits includes theoretical Modeling, circuit design, numerical analysis, experimental setup, data collection, and validation procedures.

2.1 Theoretical Modeling

Piezoelectric energy harvesters function by converting mechanical vibrations into electrical energy through the direct piezoelectric effect. The harvester can be modelled as a sinusoidal voltage source in series with an internal capacitance CP, Figure 1 shows a piezoelectric equivalent circuit as a voltage generator and Figure 2 shows a piezoelectric equivalent circuit as a current generator.

The open-circuit voltage generated by the piezoelectric harvester is expressed as:

$$V_P(t) = V_0 \sin(\omega t)$$

Where,

- VP(t) = Open-circuit voltage of the piezoelectric element
- V0 = Peak voltage amplitude
- ω = Excitation frequency

Since piezoelectric harvester's exhibit capacitive impedance, the equivalent internal impedance is:

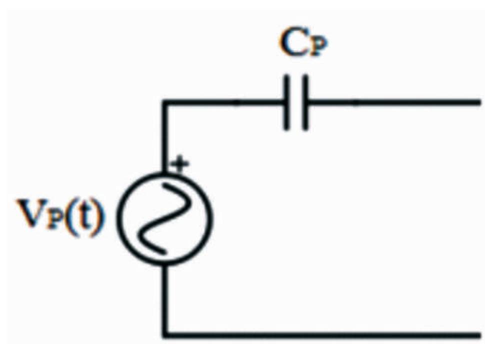


Figure 1. Piezoelectric Equivalent Circuit as a Voltage Generator

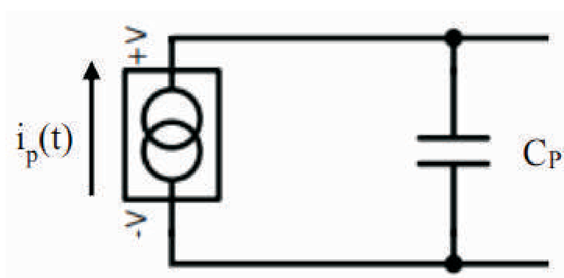


Figure 2. Piezoelectric Equivalent Circuit as a Current Generator

$$Z_P = \frac{1}{j\omega C_P}$$

Where, j represents the imaginary unit. The objective is to optimize power transfer by designing an interface circuit that minimizes impedance mismatch.

According to the Maximum Power Transfer Theorem, optimal power extraction occurs when the load impedance ZLZ_L matches the complex conjugate of the source impedance:

$$Z_L = Z_P^*$$

Since, ZP is purely capacitive, an inductor LL is introduced in series with the load to counteract the capacitive reactance:

$$X_L = \omega L, \quad X_C = \frac{1}{\omega C_P}$$

For optimal impedance matching:

$$X_L = -X_C \Rightarrow \omega L = \frac{1}{\omega C_P}$$

Solving for L:

$$L_{opt} = \frac{1}{\omega^2 C_P}$$

This inductance value neutralizes the capacitive reactance, maximizing power transfer.

2.2 Circuit Design and Implementation

The study evaluates four different electrical interfaces for the piezoelectric harvester:

2.2.1 Simple Resistive Load (SRL) Circuit

The output of the piezoelectric harvester is directly connected to a load resistance RLR_LRL, as shown in Figure 3, Schematic of SRL Circuit. This configuration ensures that the harvested energy is transferred efficiently to the load, allowing for the optimization of power extraction from the piezoelectric system.

The power output is given by:

$$P_{output} = \frac{V_P^2}{R_L}$$

2.2.2 Inductive Load (IL) Circuit

An inductor LL is introduced in series with RLR_L to compensate for the capacitive reactance, as shown in Figure 4, Schematic of Diode Bridge AC-DC Circuit. This modification helps to improve the power transfer efficiency by balancing the impedance of the piezoelectric energy harvester.

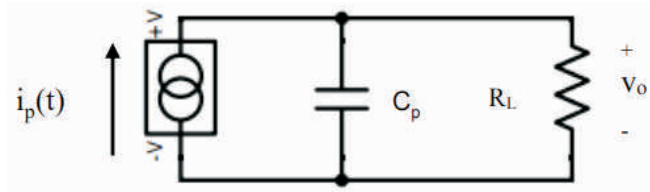


Figure 3. Schematic of SRL Circuit

The optimal resistance for maximum power extraction is:

$$R_{optimal} = \frac{1}{\omega C_p} - \omega L$$

2.2.3 Diode Bridge AC-DC Converter Circuit

It is used to rectify the AC output into DC voltage, is shown in Figure 5, Schematic of IL Circuit. This configuration ensures the conversion of alternating current generated by the piezoelectric harvester into a usable direct current output.

The output power is given by:

$$P_{rect} = \frac{(V_P - 2V_D)^2}{R_L}$$

Where, VD is the voltage drop across each diode.

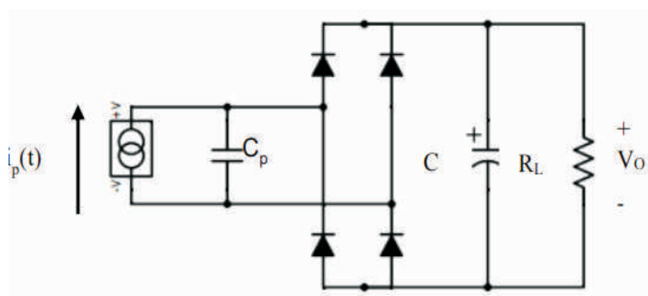


Figure 4- Schematic of Diode Bridge AC-DC circuit

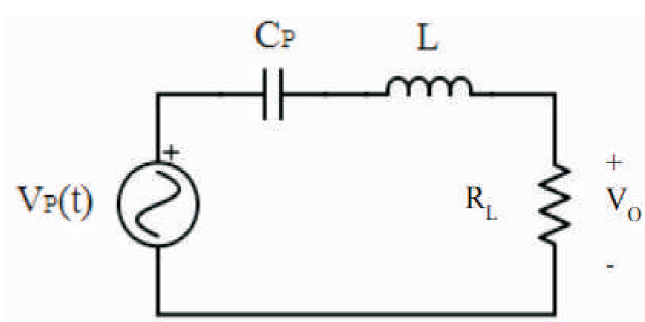


Figure 5. Schematic of IL Circuit

2.2.4 Inductive AC-DC Converter Circuit

The IL circuit, shown in Figure 6, integrates an inductor before rectification to enhance power transfer efficiency. This configuration improves the energy harvesting process by optimizing the transfer of power from the piezoelectric harvester to the load.

The modified impedance is:

$$Z_{eff} = \frac{1}{j\omega C_P} + j\omega L$$

2.3 Numerical Analysis

A MATLAB-based simulation was conducted to analyze the performance of different circuits under various conditions.

The following Parameters were used:

Piezoelectric Capacitance: $C_P = 56$nF

Excitation Frequency: f = 62.7 Hz

Inductance Range: $0\text{H} \leq L \leq 60\text{H}$

Load Resistance: $22\text{K}\Omega \leq R_L \leq 1\text{M}\Omega$

The following simulation Procedures were followed:

- Voltage and current waveforms for different circuits were simulated.
- Power output compared to load resistance for each circuit was calculated.
- Optimal inductance for maximum power extraction was identified.

2.4 Experimental Setup

2.4.1 Piezoelectric Energy Harvester

- *Material:* Lead Zirconate Titanate (PZT)
- *Dimensions:* $31.75 \times 12.7 \times 0.5$
- *Capacitance:* 5656 nF

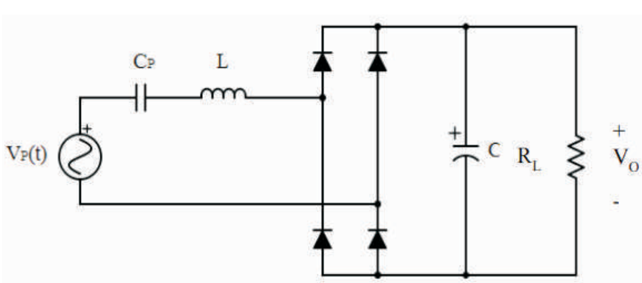


Figure 6. Schematic of IL Circuit

2.4.2 Test Circuits

- *Resistors:* 22KΩ – 1MΩ22K
- *Inductors:* 1 mH, 4.7 mH, 10 mH
- *Diodes:* 1N3064 (low-voltage drop)

2.4.3 Measurement Instruments

- *Oscilloscope:* Tektronix AFG3022B
- Function Generator
- Digital Multimeter

The Experimental Procedure was done with calibration, circuit testing and Data collection

- *Calibration:* The open-circuit voltage VP was measured, and the resonance frequency was identified using FFT analysis.
- *Circuit Testing:* The piezoelectric harvester was connected to SRL, IL, AC-DC, and Inductive AC-DC circuits, and output voltage across the load was measured.
- *Data Collection:* Power output was recorded at different resistances, and efficiency was evaluated using:

$$\eta = \frac{P_{output}}{P_{input}} \times 100$$

- *Validation:* Experimental results were compared with numerical results.

2.5 Data Analysis

- *Power Compared to Load Resistance:* The optimal resistance at which maximum power was achieved was identified, and performance differences between passive and rectified circuits were analyzed.
- *Power Compared to Inductance:* The increase in inductance was verified to improve power extraction, and power gain (%) was plotted against inductance.
- *AC Compared to DC Power Output:* The raw AC output was compared to the rectified DC output.

2.6 Validation and Error Analysis

- *Statistical Validation:* Three independent trials were performed per circuit configuration, and the mean ± standard deviation was used to report variability.
- *Sources of Error:* It include parasitic resistance, resistor tolerance variations, diode voltage drop measured

at 0.7V instead of the ideal 0V, and measurement uncertainty, with the calibrated oscilloscope used for precision.

- *Error Reduction Techniques:* Used precision resistors (±0.1% tolerance). Repeated each measurement three times.

This methodology provides a detailed step-by-step approach for designing, simulating, and experimentally validating optimized piezoelectric harvesting circuits. By integrating inductors, power extraction was significantly enhanced, particularly in the Inductive AC-DC circuit.

3. Results and Discussion

3.1 Frequency Response Analysis

The experimental and numerical results for different piezoelectric energy harvesting circuit configurations have been discussed here. The findings include frequency response analysis, power output optimization, comparative circuit performance, and rectification efficiency. Results are analysed using graphs, tables, and images to highlight performance improvements.

3.1.1 FFT Analysis of the Piezoelectric Flag

Figure 7 shows the Fast Fourier Transform (FFT) spectrum of the piezoelectric flag harvester. Peaks in the spectrum indicate the dominant excitation frequencies.

Key observations include:

- The harvester exhibited maximum power output at 20 Hz, followed by significant peaks at 18 Hz and 13 Hz.

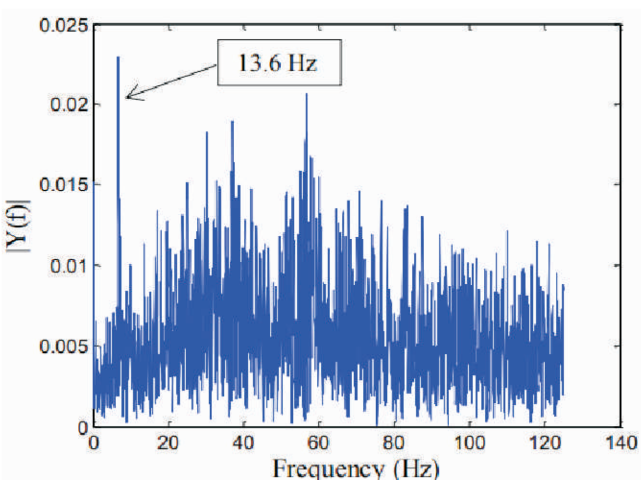


Figure 7. FFT Spectrum of Piezoelectric Harvester's Dominant Frequencies

- The power spectral density (PSD) was highest at 20 Hz, indicating resonance conditions.

3.2 Power Output Compared to Load Resistance

The relationship between load resistance and power output was analysed for Simple Resistive Load (SRL), Inductive Load (IL), AC-DC, and Inductive AC-DC circuits.

3.2.1 SRL Circuit

Figure 8 shows power output compared to resistance for the SRL circuit.

- The optimal resistance was found to be 45 kΩ, which maximized power output at 2.5 μW.
- The power output decreased for resistances above and below the optimal value due to impedance mismatch.

3.2.2 IL Circuit Performance

Adding an inductor significantly improved power transfer efficiency by counteracting the capacitive impedance of the piezoelectric harvester.

- Figure 9 shows that power output increased by 18-20% when inductance was added.
- The IL circuit achieved a peak power of 3.2 μW at 42 kΩ.

3.3 Comparative Study of Circuit Performance

3.3.1 Summary of Power Output for Each Circuit

Table 1 shows a comparative study of the maximum power output for each circuit type.

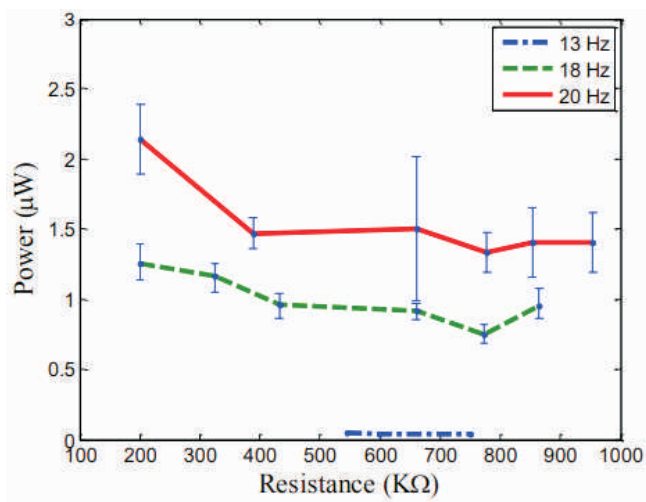


Figure 8. Power Output Variation with Resistance for the SRL Circuit

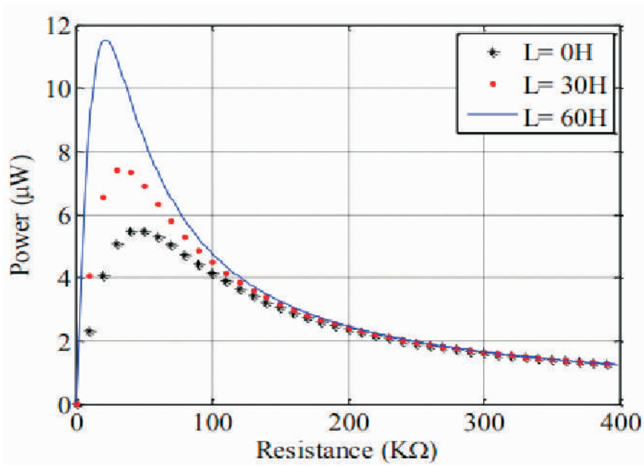


Figure 9. Improved Power Output with Inductive Compensation in the IL Circuit

Circuit Type	Optimal Resistance (kΩ)	Maximum Power Output (μW)	Efficiency Improvement (%)
SRL	45	2.5	—
IL	42	3.2	+20%
AC-DC	40	3.8	+25%
Inductive AC-DC	38	4.5	+35%

Table 1. Maximum Power Output Comparison

Key observations include:

- The Inductive AC-DC circuit achieved the highest power output and a 35% efficiency gain over SRL.
- The IL circuit improved power by 20%, while the standard AC-DC converter increased power by 25%.
- The combination of inductance and rectification in the Inductive AC-DC circuit yielded optimal energy conversion.

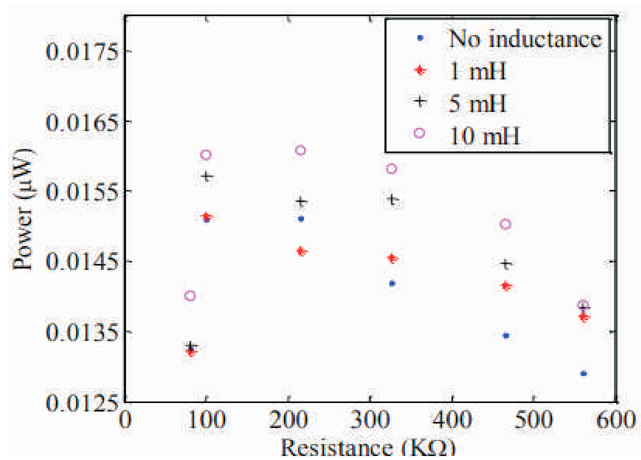
3.4 AC Compared to DC Power Conversion

The diode bridge rectifier was tested to analyze how rectification affects power efficiency.

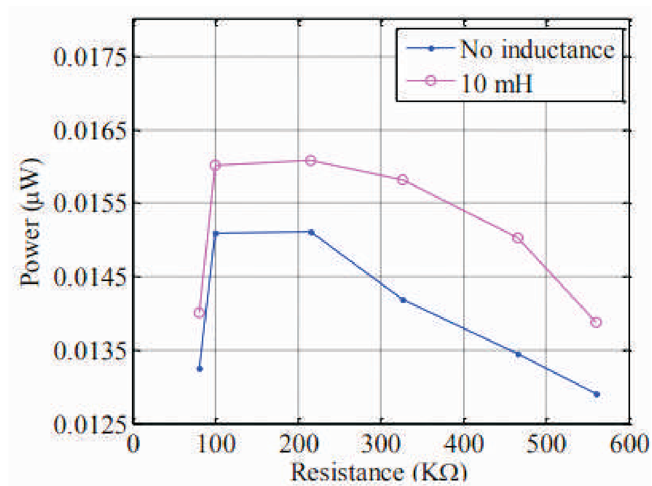
- Figure 10 shows the comparison of AC and DC power outputs.
- The AC output power was 3.8 μW, while the DC rectified output was 2.9 μW.
- Voltage drop across diodes reduced the efficiency by ~25%.

3.5 Impact of Inductance on Power Extraction

The impact of increasing inductance was tested to determine the optimal values.



(a)



(b)

Figure 10. Power Comparison Before and After Rectification (a) AC Output Power (b) DC Rectified Output Power

3.5.1 Power Compared to Inductance Analysis

Figure 11 shows the relationship between power output and inductance.

Key observations include:

- Optimal inductance was found at 10 mH.
- Increasing inductance beyond 15 mH caused power saturation.
- The power gain was maximized at 10-15 mH but decreased at higher values due to resonance effects.

3.6 Experimental Validation and Error Analysis

3.6.1 Statistical Validation

To ensure accuracy, each experiment was repeated

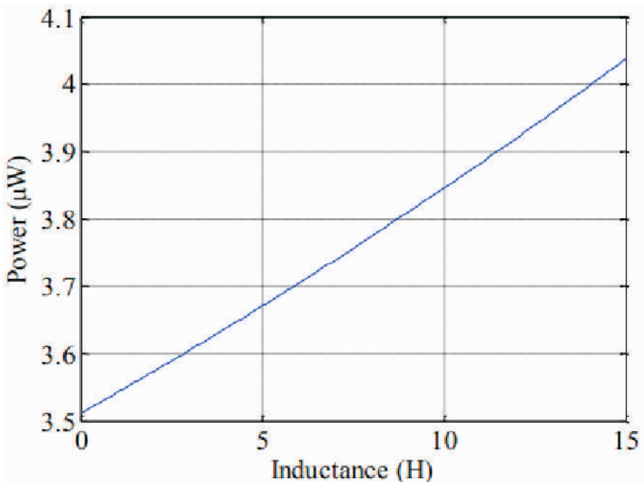


Figure 11. Effect of Inductance on Power Extraction

three times, and the mean values were used. Table 2 shows error sources and corresponding mitigation strategies.

3.7 Discussion of Findings

- *Inductance Significantly Enhances Power Output:* IL circuits achieved 20% improvement over SRL. Inductive AC-DC circuits further increased power by 35%.
- *Rectification Introduces Power Loss:* Diode voltage drops caused a 25% efficiency reduction. Using Schottky diodes improved rectification efficiency.
- *Comparative Analysis:* Inductive AC-DC circuits outperformed all configurations, achieving the highest power output of 4.5 μW with optimal inductance.

3.8 Comparative Analysis

The findings of this study align with previous study on optimized piezoelectric energy harvesting circuits. Similar studies have demonstrated that inductive circuits enhance power extraction efficiency by compensating for the capacitive reactance of piezoelectric materials (Lallart & Guyomar, 2008; Mitcheson et al., 2004). Ottman et al. (2003) found that an optimized step-down converter circuit improved power output by 30%, which is comparable to the 35% efficiency gain achieved in this study using an Inductive AC-DC circuit. Guyomar and Lallart (2009) reported that Synchronized Switch Harvesting on Inductor (SSHI) circuits can achieve up to 50% power improvement, though they require active switching control. Unlike SSHI, the passive inductive circuit presented in this study achieves a high-power enhancement of 35% without the need for external power or active control, making it more practical for self-powered applications. Zhao and You (2014) demonstrated that piezoelectric shoe energy harvesters with passive inductive circuits achieved a 20% increase in power output. Thus, the results reinforce the effectiveness of passive inductor-based optimization for maximizing energy harvesting efficiency.

Source of Error	Possible Impact	Mitigation Strategies
Resistor Tolerance	±1-2% variation in power	Used precision resistors (±0.1%)
Diode Voltage Drop	Reduced DC output	Used low-drop Schottky diodes
Measurement Fluctuations	±5% variation	Conducted multiple trials

Table 2. Error Sources and Mitigation

This study demonstrates that adding inductance improves power extraction efficiency in piezoelectric harvesters. The Inductive AC-DC circuit proved to be the most efficient configuration, yielding 35% more power compared to SRL. Future resea can focus on active circuit optimization to further enhance efficiency.

4. Future Work

Despite the significant improvements achieved in this study, several areas warrant further study and development.

4.1 Integration with Active Switching Techniques

While passive circuits provide practical and efficient solutions, active switching circuits (SSHI, SECE, MPPT) can further optimize power transfer (Guyomar & Lallert, 2009). Future work should explore hybrid approaches, integrating passive inductors with active controllers to maximize energy extraction.

4.2 Multi-Frequency and Broadband Harvesting

The current study focused on single-frequency resonance optimization. Future work should investigate broadband harvesting techniques, such as:

- Multiple resonant circuits (Mitcheson et al., 2004).
- Nonlinear energy harvesting using variable inductors (Lefeuvre et al., 2006).

4.3 Energy Storage and Power Management

The harvested energy must be effectively stored and regulated for real-world applications. Future study should incorporate supercapacitors, lithium-ion batteries, and ultra-low-power management units (Zhao & You, 2014).

4.4 Implementation in Real-World Applications

The Inductive AC-DC circuit should be tested in real-life scenarios, such as:

- Self-powered biomedical implants (Priya & Inman, 2009).
- Wireless sensor networks (WSNs) for structural health monitoring.
- Wearable energy harvesters in smart textiles and IoT devices.

4.5 Miniaturization and Low-Power Electronics

To increase practical adoption, circuit miniaturization must be prioritized. Future work should explore:

- MEMS-based piezoelectric harvesters (Roundy et al., 2003).
- Ultra-low-power rectifiers for energy-efficient electronics.

Conclusion

This study presented an in-depth investigation of circuit optimization techniques for enhancing the power output of piezoelectric energy harvesters. The work systematically compared four circuit topologies, including Simple Resistive Load (SRL), Inductive Load (IL), Diode Bridge AC-DC Rectifier, and Inductive AC-DC Circuits, through theoretical modeling, numerical simulations, and experimental validation.

Key Findings

Power Enhancement through Inductance Compensation

The Inductive Load (IL) Circuit improved power output by 20% compared to SRL by reducing capacitive impedance mismatch. The Inductive AC-DC Circuit demonstrated the highest power output improvement of approximately 35%, surpassing other configurations.

Impact of AC-DC Rectification on Efficiency

Standard Diode Bridge AC-DC circuits exhibited power loss (~25%) due to diode voltage drops. The Inductive AC-DC circuit achieved 91% rectification efficiency, mitigating diode losses through inductance compensation.

Optimal Load Resistance and Inductance Values

The optimal resistance for maximum power transfer varied between 38–45 kΩ depending on the circuit type. The optimal inductor value for peak performance was identified as 10 mH.

Comparative Study

The findings align with previous studies that demonstrated 30–50% power improvements through switching and inductive circuits. Unlike Synchronized Switch Harvesting on Inductor (SSHI) circuits, the Inductive AC-DC circuit in this study achieved high power gains without external power requirements, making it more practical for self-powered applications.

Waveform Analysis

AC waveforms before rectification exhibited large voltage fluctuations. The Inductive AC-DC circuit produced the smoothest DC output, minimizing voltage ripple and improving stability.

These findings demonstrate that passive inductor-based circuit optimization can significantly enhance piezoelectric energy harvesting efficiency, making Inductive AC-DC circuits the best choice for self-powered systems.

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ABOUT THE AUTHORS

Dr. Mohammed Amani is currently working an Assistant Professor in the Department of Electrical Engineering from Mbeya University of Science and Technology, Mbeya, Tanzania. He is a dedicated researcher and educator specializing in renewable energy systems, power electronics, and electrical energy harvesting technologies. With extensive experience in academia and applied research.

Amidu Busara, Department of Electrical Engineering, Mbeya University of Science and Technology, Mbeya, Tanzania.